

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Mikio Oda et al.

Examiner: Michael B. Singleton

Serial No.: 10/584,735

Art Unit: 2815

Filed: June 26, 2006

Docket: 19993

For: OPTICAL-ELEMENT INTEGRATED
SEMICONDUCTOR INTEGRATED
CIRCUIT AND FABRICATION METHOD
THEREOF

Dated: July 29, 2009

Conf. No.: 8209

Commissioner for Patents
P.O. Box 1450
Alexandria, VA. 22313-1450

AMENDMENT AND RESPONSE UNDER 37 C.F.R. §1.111

Sir:

In response to an Office Action of the U.S. Patent and Trademark Office mailed on April 29, 2009, please amend the subject application as follows:

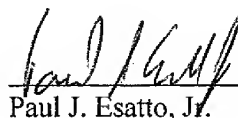
Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 9 of this paper.

CERTIFICATE OF ELECTRONIC FILING

I hereby certify that this correspondence is being deposited with the United States Patent & Trademark Office via Electronic Filing through the United States Patent and Trademark Office e-business website, on July 29, 2009.

Dated: July 29, 2009


Paul J. Esatto, Jr.